

# COMMISSION OF THE EUROPEAN COMMUNITIES

COM(80) 838 final

Brussels, 9 December 1980

Draft

## COUNCIL REGULATION (EEC)

temporarily suspending the autonomous Common Customs Tariff duties  
on certain microelectronic products

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(submitted to the Council by the Commission)

COM(80) 838 final

EXPLANATORY MEMORANDUM

1. Current technological changes particularly in the manufacture of office equipment and data-processing machinery, have led to the replacement of certain mechanical parts by microelectronic elements. Community industries must adapt to these changes if they are not to become uncompetitive.
2. For this reason the governments of several Member States requested that the duty be suspended for some electronic memories, microcomputers etc. in order to help Community user industries.
3. The Commission examined these requests with experts of national administration in the "Economic Tariff Problems Group". The group verified for every item whether and to what extent the microcircuits in question are produced in the Community. It furthermore considered whether duty suspensions for these microcircuits might not prejudice the possibility of beginning their production in the Community, this manufacture being considered as urgently desirable. The interests of Community users and producers were carefully weighed.
4. In the light of these studies the Commission proposes that a duty suspension should be granted for the goods listed in the annex to the attached draft regulation. As these goods are sensible, a period of application of six months (1.1.1981 until 30.6.1981) is proposed.
5. However, following the Group's discussions, the Commission does not think that it is advisable to propose, at present, a tariff measure for the following goods :

CCT number

Description

ex 85.21 D II

Random access memories (RAMS) of CMOS technology, with a storage capacity of 4 K bits, in the form of a monolithic integrated circuit, contained in a housing with 18, 20 or 22 connecting pins, whose exterior dimensions do not exceed 11 x 31 mm.

The package bears

- a marking either consisting of one of the following combinations of figures or figures and letters or including one of those combinations :

21 SC 14	5047	6147	8404 E
	5104	6504	8414 E
4315	5114 A	6514	
4334	5115	6533	
4434	5504	6543	
mu PD 444	5514		
mu PD 445 L	58981		

or

- other markings relating to RAMS complying with the above-mentioned description.

ex 85.21 D II

Microcomputer in the form of a monolithic integrated circuit consisting of an 8 bit central processor, an 8 bit timer, 18 input/output gates, a read only memory (ROM) with a capacity of 8 K bits and a random access memory (RAM) with a capacity of 512 bits, contained in a housing, whose exterior dimensions are 54 x 14 mm with 40 connecting pins.

The package bears

- one of the following markings :

D 8041

P 8041

or

- other markings relating to microcomputers complying with the above-mentioned description

Draft

COUNCIL REGULATION (EEC)

temporarily suspending the autonomous Common Customs Tariff duties on certain microelectronic products

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THE COUNCIL OF THE EUROPEAN COMMUNITIES,

Having regard to the Treaty establishing the European Economic Community, and in particular Article 28 thereof,

Whereas production of the products referred to in this Regulation is at present inadequate or non-existent within the Community and producers are thus unable to meet the needs of user industries of the Community;

Whereas it is in the Community's interest to suspend the autonomous Common Customs Tariff duties only partially in certain cases, owing in particular to the existence of Community production, and to suspend them totally in other cases;

Whereas, taking account of the difficulties involved in accurately assessing, in the immediate future, the development of the economic situation in the sectors concerned, these suspension measures should be taken on a temporary basis only, their period of validity being fixed to coincide with the interests of Community production,

HAS ADOPTED THIS REGULATION:

Article 1

From 1 January to 30 June 1981, the autonomous Common Customs Tariff duties for the products listed in the Annex hereto shall each be suspended at the respective level indicated.

Article 2

This Regulation shall enter into force on 1 January 1981.

This Regulation shall be binding in its entirety and directly applicable in all Member States.

Done at

For the Council  
The President

Annex

CCT number	Description	Rate of duty (%)				
ex 84.55 C	<p>Optical encoder for electronic typewriters consisting of electronic circuits, a light emitting diode (LED), a rotating disc with radial openings, a fixed mask and a single ship photovoltaic cell, contained in two plastic shells forming a housing of a size not exceeding 60 x 48 x 16 mm</p>	0				
ex 85.21 D II	<p>Electronic programmable read only memories, UV erasable (EPROMS), with a storage capacity of 16 K bits, in the form of a monolithic integrated circuit contained in a housing whose exterior dimensions do not exceed 17 x 35 mm, with a quartz window on the upper surface.</p> <p>The package bears</p> <ul style="list-style-type: none"> <li>- a marking either consisting of one of the following combinations of figures or figures and letters or including one of those combinations :               <ul style="list-style-type: none"> <li>2516</li> <li>2716</li> <li>27 A 16</li> <li>TMN 323</li> <li>F 68716</li> </ul> </li> <li>or</li> <li>- other markings relating to EPROMS complying with the above-mentioned description</li> </ul>	11.5				
ex 85.21 D II	<p>Electronic programmable read only memories, UV erasable (EPROMS), with a storage capacity of 32 K bits in the form of a monolithic integrated circuit contained in a housing whose exterior dimensions do not exceed 17 x 39 mm, with a quartz window on the upper surface.</p> <p>The package bears</p> <ul style="list-style-type: none"> <li>- a marking either consisting of one of the following combinations of figures or figures and letters or including one of those combinations :               <table border="0" style="margin-left: 20px;"> <tr> <td style="padding-right: 20px;">2532</td> <td>25 L 32</td> </tr> <tr> <td>25 A 32</td> <td>2732</td> </tr> </table> </li> <li>- other marking relating to EPROMS complying with the above-mentioned description</li> </ul>	2532	25 L 32	25 A 32	2732	0
2532	25 L 32					
25 A 32	2732					

CCT number	Description	Rate of duty (%)
ex 85.21 D II	<p>Electronic programmable read only memories, UV erasable (EPROMS) , with a storage capacity of 64 K bits in the form of a monolithic integrated circuit contained in a housing whose exterior dimensions do not exceed 17 x 39 mm, with a quartz window on the upper surface.</p> <p>The package bears</p> <ul style="list-style-type: none"> <li>- a marking either consisting of one of the following combinations of figures or figures and letters or including one of those combinations :</li> </ul> <p>2564 68764 68 A 764</p> <p>or</p> <ul style="list-style-type: none"> <li>- other markings relating to EPROMS complying with the above-mentioned description</li> </ul>	0
ex 85.21 D II	<p>Random access memories (RAMS) with a storage capacity of 64 K bits, in the form of a monolithic integrated circuit contained in a housing whose exterior dimensions do not exceed 9 mm x 23 mm with 16 connecting pins.</p> <p>The package bears</p> <ul style="list-style-type: none"> <li>- a marking either consisting of one of the following figures or including one of those:</li> </ul> <p>3764 4164 4864 6664 6665 8164</p> <p>or</p> <ul style="list-style-type: none"> <li>- other markings relating to RAMS complying with the above-mentioned description</li> </ul>	5

CCT number	Description	Rate of duty (%)																																													
ex 85.21 D II	<p>Random access memories (RAMS) of CMOS technology with a storage capacity of 1 K bit in the form of a monolithic integrated circuit, contained in a housing whose exterior dimensions do not exceed 11x31 mm with 16, 18 or 22 connecting pins.</p> <p>The package bears</p> <ul style="list-style-type: none"> <li>- a marking either consisting of one of the following combinations of figures or figures and letters or including one of those combinations :</li> </ul> <table border="1" data-bbox="363 691 942 1190"> <tr> <td>1821 C</td> <td>5508</td> <td>74 C 921</td> </tr> <tr> <td>1822</td> <td>MSM 573</td> <td>74 C 929</td> </tr> <tr> <td></td> <td>MSM 574</td> <td>74 C 930</td> </tr> <tr> <td>mu PD 443.</td> <td></td> <td></td> </tr> <tr> <td></td> <td>6501</td> <td></td> </tr> <tr> <td>5001</td> <td>6508</td> <td></td> </tr> <tr> <td>5101</td> <td>6518</td> <td></td> </tr> <tr> <td>510 E 1</td> <td>6551</td> <td></td> </tr> <tr> <td>5102</td> <td>6552</td> <td></td> </tr> <tr> <td>54 C 920</td> <td>6561</td> <td></td> </tr> <tr> <td>54 C 921</td> <td>6562</td> <td></td> </tr> <tr> <td>54 C 929</td> <td></td> <td></td> </tr> <tr> <td>54 C 930</td> <td>74920</td> <td></td> </tr> <tr> <td>5501</td> <td>74921</td> <td></td> </tr> <tr> <td></td> <td>74 C 920</td> <td></td> </tr> </table> <p>or</p> <ul style="list-style-type: none"> <li>- other markings relating to RAMS complying with the above-mentioned description</li> </ul>	1821 C	5508	74 C 921	1822	MSM 573	74 C 929		MSM 574	74 C 930	mu PD 443.				6501		5001	6508		5101	6518		510 E 1	6551		5102	6552		54 C 920	6561		54 C 921	6562		54 C 929			54 C 930	74920		5501	74921			74 C 920		8.5
1821 C	5508	74 C 921																																													
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510 E 1	6551																																														
5102	6552																																														
54 C 920	6561																																														
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54 C 930	74920																																														
5501	74921																																														
	74 C 920																																														
ex 85.21 D II	<p>Programmable read only memories (PROMS) with a storage capacity of 4 K bits in the form of a monolithic integrated circuit contained in a housing whose exterior dimensions do not exceed 17 x 34 mm, with not more than 24 connecting pins.</p> <p>The package bears</p> <ul style="list-style-type: none"> <li>- a marking either consisting of one of the following combinations of figures or figures and letters or including one of those combinations :</li> </ul>																																														

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CCT number	Description	Rate of duty (%)
18 S 42	5340	7640
18 S 46	5341	7641
18 SA 42	5348	7642
18 SA 46	5349	7643
	5350	7644
24 S 41	5351	7645
24 SA 41	5352	7647
	5353	7648
27 S 15		7649
27 S 26	54 S 472	
27 S 27	54 S 473	82 S 115
27 S 28	54 S 474	82 S 136
27 S 29	54 S 475	82 S 137
27 S 30	54 S 476	82 S 140
27 S 31	54 S 477	82 S 141
27 S 32	54 S 572	82 S 142
27 S 33	54 S 573	82 S 146
	54 740	82 S 147
	54 741	
28 L 42		
28 L 45		93 438
28 P 42	5605	93 448
28 P 45	5625	93 452
28 R 45		93 453
28 S 42	6340	
28 S 45	6341	
	6348	
29620	6349	
29621	6350	
29622	6351	
29623	6352	
29624	6353	
29625		
29626	7121	
29627	7122	
	7123	
3604	7125	
3624	7126	
3625		
	74 S 472	
38510	74 S 473	
	74 S 474	
	74 S 475	
	74 S 476	
	74 S 477	
	74 S 572	
	74 S 573	
or		
- other markings relating to PROMS complying with the above-mentioned description		



CCT number	Description	Rate of duty (%)				
ex 85.21 D II	<p>Programmable read only memories (PROMS) with a storage capacity of 8 K bits in the form of a monolithic integrated circuit, contained in a small housing whose exterior dimensions do not exceed 17 x 34 mm, with not more than 24 connecting pins.</p> <p>The package bears</p> <ul style="list-style-type: none"> <li>- a marking either consisting of one of the following combinations of figures or figures and letters or including one of those combinations :</li> </ul>					
	<table border="0" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 25%; vertical-align: top;">                 24 S 81                  24 SA 81                   2708                   27 S 180                  27 S 181                   28 L 85                  28 L 86                   28 P 85                  28 R 85                  28 S 85                  28 S 86                  28 SA 86                   29630                  29631                  29632                  29633                  29634                  29635                  29636                  29637             </td> <td style="width: 25%; vertical-align: top;">                 29650                  29651                  29652                  29653                   3628                   5380                  5381                   54 LS 478                  54 S 454                  54 S 455                  54 S 478                  54 S 479                   6380                  6381                   7127                  7128                  7129                  7130                  7131                  7132             </td> <td style="width: 25%; vertical-align: top;">                 74 LS 478                  74 S 454                  74 S 455                  74 S 478                  74 S 479                   7608                  7680                  7681                  7684                  7685                  7686                  7687                   77 S 180                  77 S 181                  77 S 184                  77 S 185                  77 S 186                  77 S 187                   93450                  93451                  93460                  93461                  93465                  93466                  93 L 450                  93 L 451                   9460             </td> <td style="width: 25%; vertical-align: top;">                 TRPS 81 M                  82707                  82 LS 180                  82 LS 181                  82 S 180                  82 S 181                  82 S 182                  82 S 183                  82 S 184                  82 S 185                   87 S 180                  87 S 181                  87 S 184                  87 S 185                  87 S 186                  87 S 187                   93450                  93451                  93460                  93461                  93465                  93466                  93 L 450                  93 L 451                   9460             </td> </tr> </table>	24 S 81 24 SA 81  2708  27 S 180 27 S 181  28 L 85 28 L 86  28 P 85 28 R 85 28 S 85 28 S 86 28 SA 86  29630 29631 29632 29633 29634 29635 29636 29637	29650 29651 29652 29653  3628  5380 5381  54 LS 478 54 S 454 54 S 455 54 S 478 54 S 479  6380 6381  7127 7128 7129 7130 7131 7132	74 LS 478 74 S 454 74 S 455 74 S 478 74 S 479  7608 7680 7681 7684 7685 7686 7687  77 S 180 77 S 181 77 S 184 77 S 185 77 S 186 77 S 187  93450 93451 93460 93461 93465 93466 93 L 450 93 L 451  9460	TRPS 81 M 82707 82 LS 180 82 LS 181 82 S 180 82 S 181 82 S 182 82 S 183 82 S 184 82 S 185  87 S 180 87 S 181 87 S 184 87 S 185 87 S 186 87 S 187  93450 93451 93460 93461 93465 93466 93 L 450 93 L 451  9460	
24 S 81 24 SA 81  2708  27 S 180 27 S 181  28 L 85 28 L 86  28 P 85 28 R 85 28 S 85 28 S 86 28 SA 86  29630 29631 29632 29633 29634 29635 29636 29637	29650 29651 29652 29653  3628  5380 5381  54 LS 478 54 S 454 54 S 455 54 S 478 54 S 479  6380 6381  7127 7128 7129 7130 7131 7132	74 LS 478 74 S 454 74 S 455 74 S 478 74 S 479  7608 7680 7681 7684 7685 7686 7687  77 S 180 77 S 181 77 S 184 77 S 185 77 S 186 77 S 187  93450 93451 93460 93461 93465 93466 93 L 450 93 L 451  9460	TRPS 81 M 82707 82 LS 180 82 LS 181 82 S 180 82 S 181 82 S 182 82 S 183 82 S 184 82 S 185  87 S 180 87 S 181 87 S 184 87 S 185 87 S 186 87 S 187  93450 93451 93460 93461 93465 93466 93 L 450 93 L 451  9460			
	<p>or</p> <ul style="list-style-type: none"> <li>- other markings relating to PROMS complying with the above-mentioned description</li> </ul>	0				

CCT number	Description	Rate of duty (%)																																								
ex 85.21 D II	<p>Microcomputer, in the form of a monochip and in the form of a monolithic integrated circuit, consisting of an arithmetical unit with a capacity of 4 bits plus a read only memory (ROM) with a capacity of not less than 18 K bits and not more than 65 K bits and a random access memory (RAM) with a capacity of not less than 512 bits and not more than 4 K bits, contained in a housing whose exterior dimensions are not exceeding 16 x 54 mm with not more than 40 connecting pins.</p> <p>The package bears</p> <ul style="list-style-type: none"><li>- one of the following markings :</li></ul> <table data-bbox="417 780 743 1102"><tr><td>CD</td><td>3200</td><td>until</td><td>3299</td></tr><tr><td>TMC</td><td>0270</td><td>until</td><td>0279</td></tr><tr><td>TMC</td><td>0500</td><td>until</td><td>0599</td></tr><tr><td>TMC</td><td>0980</td><td>until</td><td>0989</td></tr><tr><td>TMC</td><td>1500</td><td>until</td><td>1599</td></tr><tr><td>TMC</td><td>1980</td><td>until</td><td>1999</td></tr><tr><td>TP</td><td>0310</td><td>until</td><td>0329</td></tr><tr><td>TP</td><td>0450</td><td>until</td><td>0459</td></tr><tr><td>TP</td><td>0480</td><td>until</td><td>0489</td></tr><tr><td>TP</td><td>0500</td><td>until</td><td>0599</td></tr></table> <p>or</p> <ul style="list-style-type: none"><li>- other markings relating to microcomputers complying with the above-mentioned description,</li></ul> <p>for the manufacture of electronic calculators (a)</p>	CD	3200	until	3299	TMC	0270	until	0279	TMC	0500	until	0599	TMC	0980	until	0989	TMC	1500	until	1599	TMC	1980	until	1999	TP	0310	until	0329	TP	0450	until	0459	TP	0480	until	0489	TP	0500	until	0599	0
CD	3200	until	3299																																							
TMC	0270	until	0279																																							
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TP	0480	until	0489																																							
TP	0500	until	0599																																							

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(a) Control of the use for this special purpose shall be carried out pursuant to the relevant Community provisions.

FINANCIAL SHEET

ANNEX to doc. SUD/1288/80 EN  
Date : 19.11.1980

1. Budget line concerned : Chap.12, Art. 20

2. Title of activity :

Draft Council Regulation (EEC) temporarily suspending the autonomous Common Customs Tariff duties on certain industrial products.

3. Legal basis : Art. 28 Treaty of Rome

4. Objectives of the activity :

Suspension of CCT duties for goods mentioned under 2. for the period 1.1 until 30.6 1981

5.0 Costs of the activity to the account of the EC budget by non collection of duties

1.1 until 30.6.1981  
Impossible to evaluate seriously because Community statistics do not exist.  
The information given by the requesting Member States shows a minimum loss of 680 000 EUA.